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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/667,029	09/18/2003	Merwin H. Alferness	ROC920030085US1	9131

30206   7590   03/21/2006

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ART UNIT	PAPER NUMBER
2182	

DATE MAILED: 03/21/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

PTO-90C (Rev. 10/03)

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE <u>3</u> MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
  Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

1)☒ Responsive to communication(s) filed on *18 September 2003*.

2a)☐ This action is **FINAL**.   2b)☒ This action is non-final.

3)☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

4)☒ Claim(s) *1-23* is/are pending in the application.

    4a) Of the above claim(s) _____ is/are withdrawn from consideration.

5)☐ Claim(s) _____ is/are allowed.

6)☒ Claim(s) *1-23* is/are rejected.

7)☐ Claim(s) _____ is/are objected to.

8)☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

9)☐ The specification is objected to by the Examiner.

10)☒ The drawing(s) filed on *18 September 2003* is/are: a)☒ accepted or b)☐ objected to by the Examiner.

    Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).

    Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).

11)☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

12)☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).

    a)☐ All   b)☐ Some * c)☐ None of:

      1.☐ Certified copies of the priority documents have been received.

      2.☐ Certified copies of the priority documents have been received in Application No. _____.

      3.☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

    * See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

1)☒ Notice of References Cited (PTO-892)

2)☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)

3)☐ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date _____.

4)☐ Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____.

5)☐ Notice of Informal Patent Application (PTO-152)

6)☐ Other: _____.

DETAILED ACTION

Claim Objections

1.   Claim 23 is objected to because of the following informalities: "plurality of ports

and memory" in line 8 should be replaced with "the plurality of ports and the memory".

Claim Rejections - 35 USC § 102

2.   The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that

form the basis for the rejections under this section made in this Office action:

> A person shall be entitled to a patent unless –
>
> (e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

3.   Claims 1-3, 5, 9, 11; 12-14, 16, 20, 22; 23 are rejected under 35 U.S.C. 102(e) as

being anticipated by Kawakami et al. (US 6,560,231).

4.   <u>As per claim 1</u>, Kawakami teaches a method [FIG. 11, FIG. 12] of self-adjusting

allocation of memory bandwidth in a network processor system comprising:

        determining an amount of memory bandwidth of a network processor used by

each of a plurality of data types [S706, FIG. 11; col. 7, lines 45-51]; and

        dynamically adjusting the amount of memory bandwidth allocated to at least one

of the plurality of data types based on the determination [S710-S708, FIG. 11; col. 7,

lines 51-57; establishing a connection resulting in $B1 \rightarrow B'1$, and dynamically adjusting

the amount of memory bandwidth allocated to the plurality of data types to $X'1$-$X'm$,

FIG. 12].

5.   As per claims 2-3, 5, Kawakami teaches a total amount of memory bandwidth of

the network processor used by the plurality of data types being configurable [available

bandwidth of transmission path used by the plurality of data types being configured to

X'1-X'm after establishing connection]; determining whether memory bandwidth may be

allocated to at least one of the plurality of data types [S704, S706, FIG. 11] including

determining whether a port for transmitting data of at least one of the plurality data types

may be activated [a connection may be established at S708, and a connection being

inhibited at S712, FIG. 11].

6.   As per claims 9, 11, Kawakami teaches the plurality of data types including an

ATM protocol data type [col. 3, lines 39-53]; and dynamically adjusting the amount of

memory bandwidth allocated to at least one of the plurality of data types based on the

determination including at least one of dynamically activating and deactivating a port for

transmitting data of at least one of the plurality of data types [S708, FIG. 11; col. 9, lines

27-29].

7.   As per claims 12-14, 16, 20, 22, Kawakami teaches an apparatus [FIGs. 3-5]

comprising a port activation logic [150, 250] adapted to couple to a memory of a

network processor and to interact with the memory [col. 3, line 66-col. 4, line 67] and

limitations that generally correspond to the limitations recited in claims 1-3, 5, 9 and 11

(see rejections of claims 1-3, 5, 9, 11 above).

8.   As per claim 23, Kawakami teaches a network processor system [FIGs. 3-5]

comprising a memory [buffers] and a network processor coupled to the memory, the

network processor comprising a memory controller [extracting block], a plurality of ports

[input connections], and a port activation logic [bandwidth managing controller] coupled

to the memory controller, the plurality of ports and the memory, and adapted to interact

with the memory and limitations that generally correspond to the limitations recited in

claim 1 (see rejections of claim 1 above).

9.   Claims 6-7, 17-18 are rejected under 35 U.S.C. 102(e) as being anticipated by

Sourani et al. (US 6,549,515).

10.   As per claims 6-7, Sourani teaches a method of self-adjusting allocation of

memory bandwidth in a network processor system comprising:

        determining an amount of memory bandwidth of a network processor used by

each of a plurality of data types including determining a number of active ports of the

network processor used to transmit data of each of the plurality of data types and

determining an amount of memory bandwidth allocated to each active port for each of

the plurality of data types [establishing a network instantaneous demand for bandwidth

by calculating the total number of bits required for the operative algorithms (data types)

in all the active channels [col. 2, lines 43-49]]; and

        dynamically adjusting the amount of memory bandwidth allocated to at least one

of the plurality of data types based on the determination [col. 2, lines 50-62].

        Sourani further teaches the amount of memory bandwidth allocated to each

active port for a data type being the same [active channels with same operative

algorithm are allocated same amount of bandwidth].

11.   As per claims 17-18, see the rejections of claims 6-7 above.

12.   Claims 1-3, 6-9, 11; 12-14, 17-20, 22-23 are rejected under 35 U.S.C. 102(e) as

being anticipated by Alferness et al. (US 2004/0017781).

The applied reference has a common inventor with the instant application.

Based upon the earlier effective U.S. filing date of the reference, it constitutes prior art

under 35 U.S.C. 102(e). This rejection under 35 U.S.C. 102(e) might be overcome

either by a showing under 37 CFR 1.132 that any invention disclosed but not claimed in

the reference was derived from the inventor of this application and is thus not the

invention "by another," or by an appropriate showing under 37 CFR 1.131.

Alferness teaches in [0040]-[0043] determining an amount of memory bandwidth

used by each of the plurality of data types [determining received bandwidth for each

virtual channel] and dynamically adjusting the amount of memory bandwidth allocated to

at least one of the plurality of the data types based on the determination [if two channels

are active, each would receive their nominal bandwidth; if one channel is active, it would

receive the entire bandwidth of the path];

the total amount of memory bandwidth for the virtual path being set up [i.e.

configured] to have an average bandwidth of 1Mbps;

determining whether memory bandwidth may be allocated to one of the plurality

of data types [when only one channel is active, it is determined that the excess

bandwidth may be allocated to allow this channel to have the full bandwidth of the

virtual data path];

determining a number of active ports and determining an amount of bandwidth

allocated to each active port [if two channels are active, each would receive their

nominal bandwidth; if one channel is active, it would receive the entire bandwidth of the

path];

the amount of memory bandwidth allocated to each active port being the same

[0.5 Mbps when two channels are active];

the bandwidth allocated to each active port being configurable [when only one

channel is active, the bandwidth allocated for the port is scaled up to 1Mbps] and the

data type being ATM data type [[0071], lines 6-8];

dynamically activating and deactivating a port [scaling up of bandwidth when

virtual path is undersubscribed and scaling down of bandwidth when virtual path is

oversubscribed].

Claim Rejections - 35 USC § 103

13.   The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all

obviousness rejections set forth in this Office action:

> (a) A patent may not be obtained though the invention is not identically disclosed or described as set
> forth in section 102 of this title, if the differences between the subject matter sought to be patented and
> the prior art are such that the subject matter as a whole would have been obvious at the time the
> invention was made to a person having ordinary skill in the art to which said subject matter pertains.
> Patentability shall not be negatived by the manner in which the invention was made.

14.   This application currently names joint inventors. In considering patentability of

the claims under 35 U.S.C. 103(a), the examiner presumes that the subject matter of

the various claims was commonly owned at the time any inventions covered therein

were made absent any evidence to the contrary. Applicant is advised of the obligation

under 37 CFR 1.56 to point out the inventor and invention dates of each claim that was

not commonly owned at the time a later invention was made in order for the examiner to

consider the applicability of 35 U.S.C. 103(c) and potential 35 U.S.C. 102(e), (f) or (g)

prior art under 35 U.S.C. 103(a).

15.   Claims 4, 10, 15, 21 are rejected under 35 U.S.C. 103(a) as being unpatentable

over Kawakami et al.

16.   As per claims 4, 15, Kawakami does not specifically teach determining whether

memory bandwidth may be allocated to at least one of the plurality of data types

includes determining a difference between a maximum amount of memory bandwidth of

the network processor that may be used by the plurality of data types and the total

amount of memory bandwidth of the network processor currently used by the plurality of

data types.

Kawakami teaches determining whether the total required bandwidth $\leq$ available

bandwidth of the transmission path [S706, FIG. 11; col. 7, lines 51-54], hence

determining whether the maximum amount of bandwidth $\geq$ the total amount of

bandwidth used. Since (the maximum amount of bandwidth $\geq$ the total amount of

bandwidth used) can be rearranged mathematically to ([the maximum amount of

bandwidth - the total amount of bandwidth used] ≥ 0), it would have been obvious to one

of ordinary skill in the art at the time the invention was made to determine whether

memory bandwidth may be allocated to at least one of the plurality of data types by

determining whether ([the maximum amount of bandwidth - the total amount of

bandwidth used] ≥ 0), as an alternative to determining whether (the maximum amount of

bandwidth $\geq$ the total amount of bandwidth) since they are mathematically equivalent

and since the invention would perform equally well using either one of the expressions.

17.   As per claims 10, 21, Kawakami does not teach the plurality of data types including an Ethernet protocol data type, wherein the Ethernet protocol data type includes at least one of a Gigabit Ethernet data type and a Fast Ethernet data type.

Since it was known in the art at the time the invention was made for Ethernet frames to ride within ATM cells to allow Ethernet frames to be transferred through an ATM medium, and since it was known in the art for Ethernet protocol to include a Gigabit Ethernet protocol for demanding applications, it would have been obvious to one of ordinary skill in the art at the time the invention was made for the plurality of data types to include an Ethernet protocol data type in order to transfer Ethernet frames with ATM cells, and for the Ethernet protocol to include a Gigabit Ethernet protocol in order to accommodate demanding applications.

18.   Claims 8, 19 are rejected under 35 U.S.C. 103(a) as being unpatentable over Sourani et al..

Sourani teaches memory bandwidth allocated to each active port being configurable [col. 2, lines 53-63], but does not teach an ATM protocol data type. Sourani, however, teaches algorithms for FAX, ADPCM.

Since it was known in the art to use ATM to transport FAX, and for converted ADPCM traffic to be inserted into an ATM stream, it would have been obvious to one of ordinary skill in the art at the time the invention was made to use ATM protocol data type in order to transport data of different types [i.e. Fax, ADPCM].

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Tanh Quang Nguyen whose telephone number is (571) 272-4154 and whose e-mail address is tanh.nguyen36@uspto.gov. The examiner can normally be reached on Monday-Friday from 8:30 AM to 5:00 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Kim Huynh, can be reached on (571) 272-4147. The fax phone number for the organization where this application or proceeding is assigned is (571) 273-8300 for After Final, Official, and Customer Services, or (571) 273-4154 for Draft to the Examiner (please label "PROPOSED" or "DRAFT").

Effective May 1, 2003 are new mailing address is:

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03/16/2006

TQN

March 16, 2006